

CONTINUED ON 002000

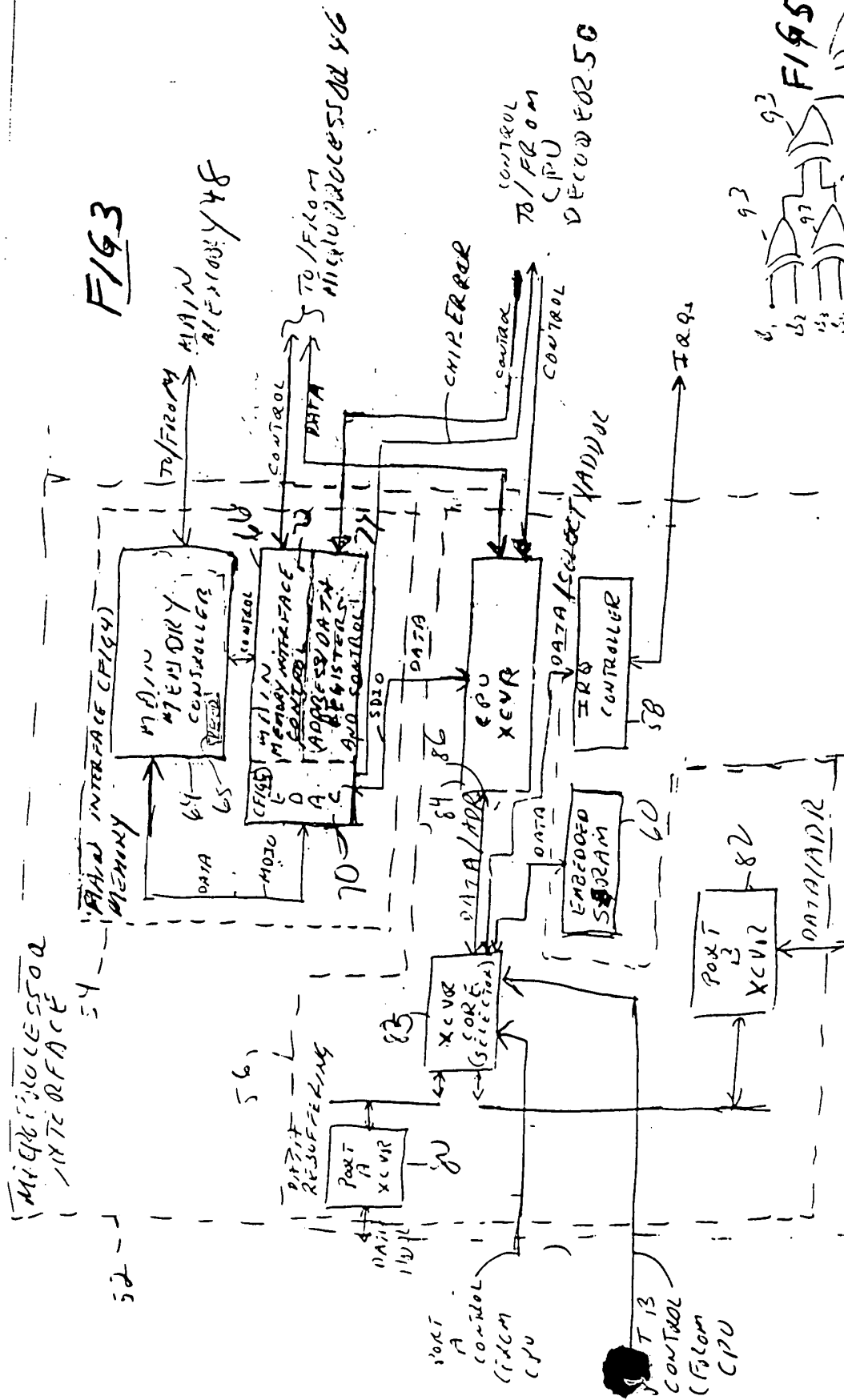
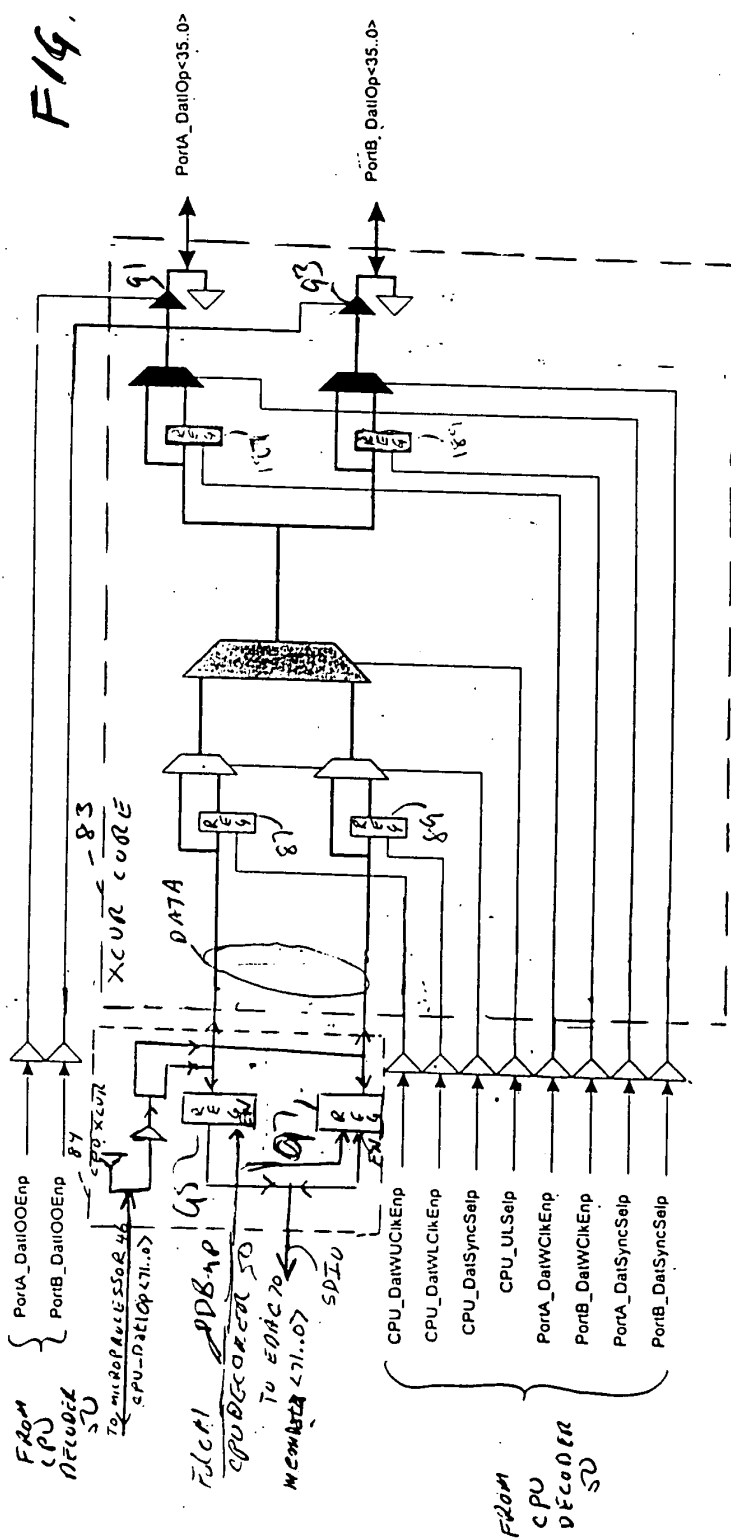
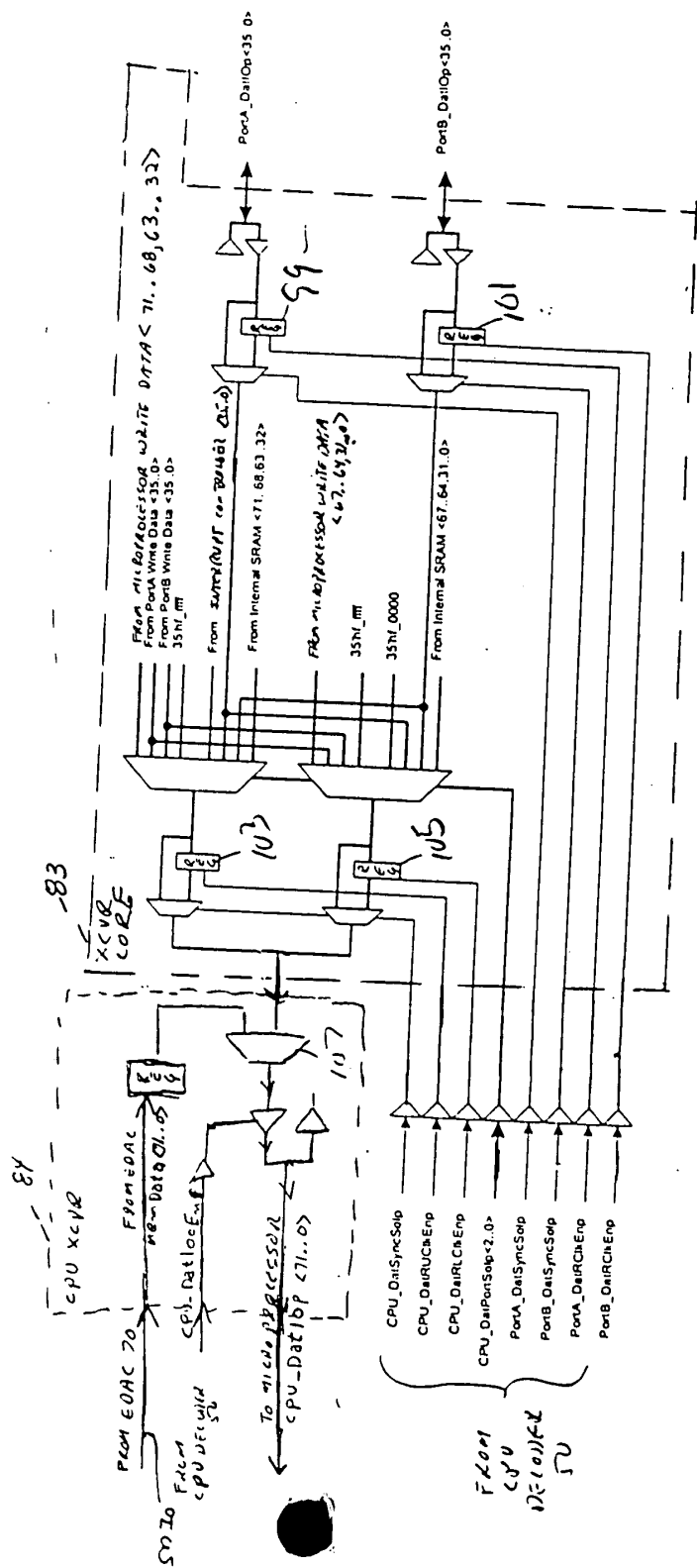
[illegible]

FIG. 3A



00000000000000000000

F/G3B



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FIG 3C

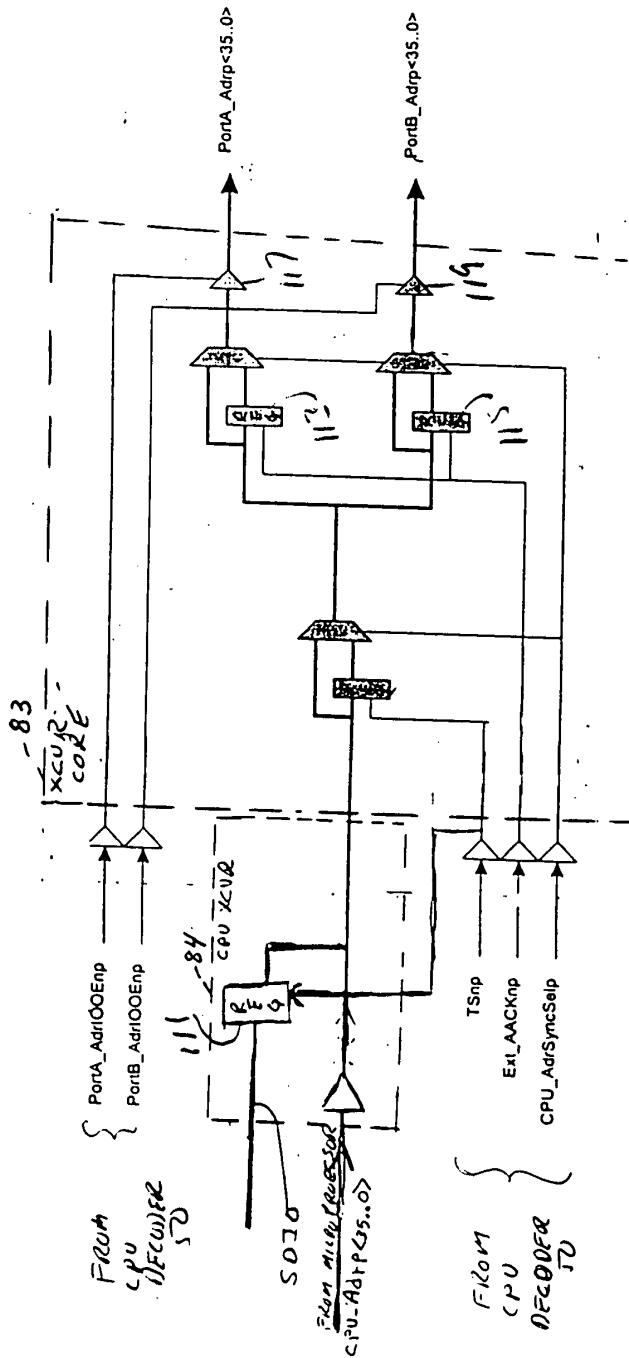
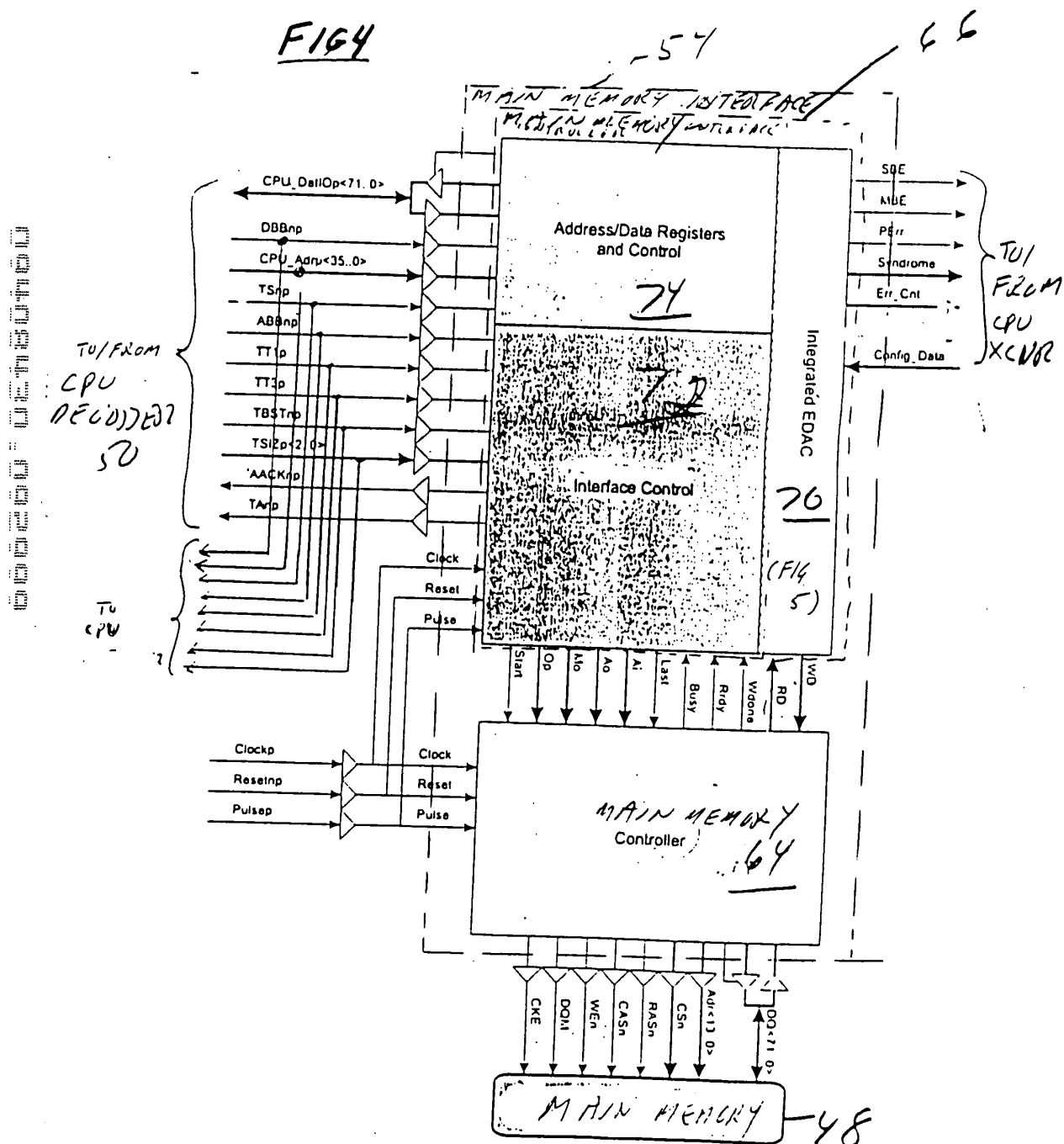


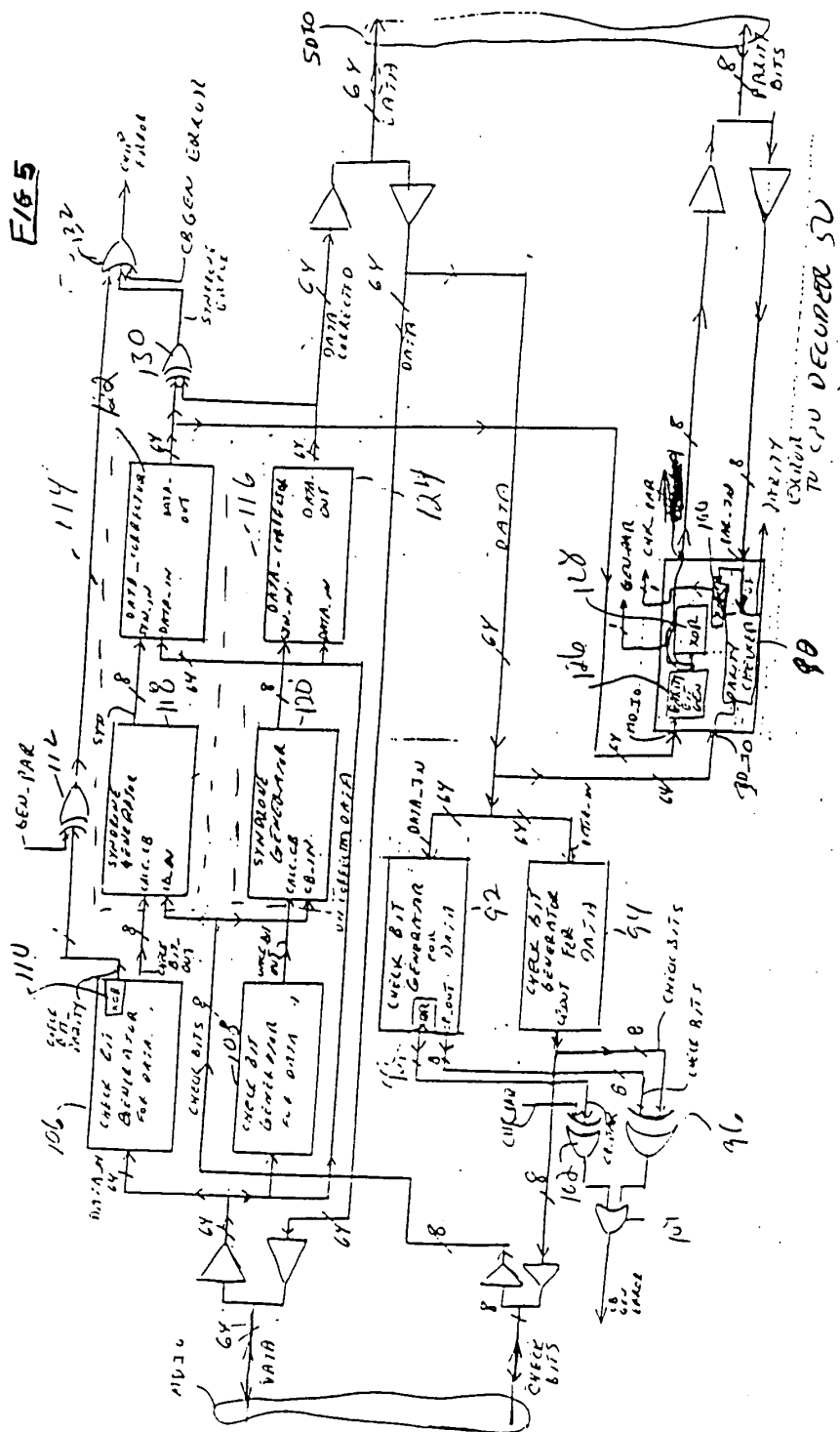
FIG 3C

FIG 4



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INTERPT CONTROLLER

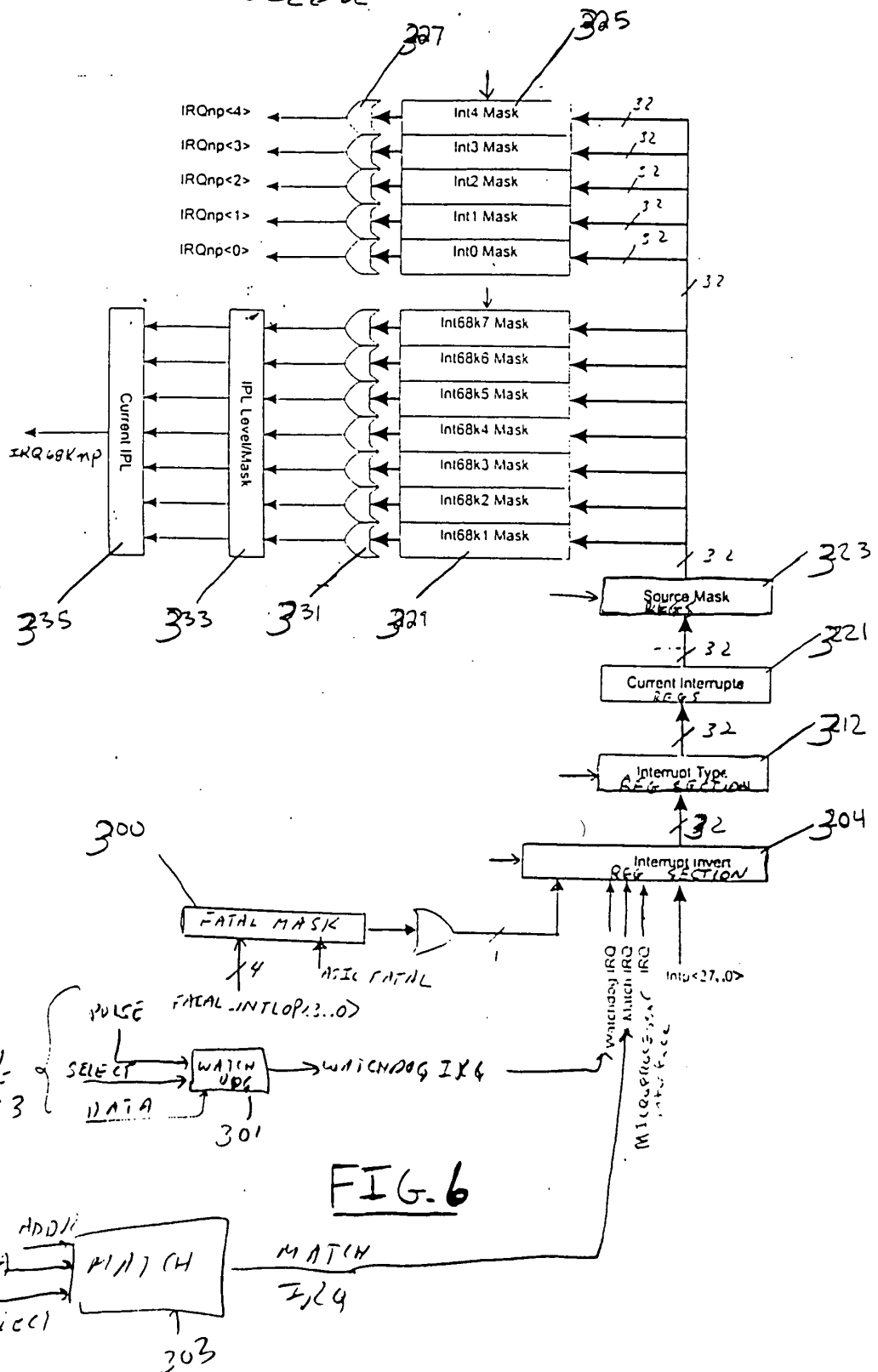


FIG. 6

[illegible]

FIG 7

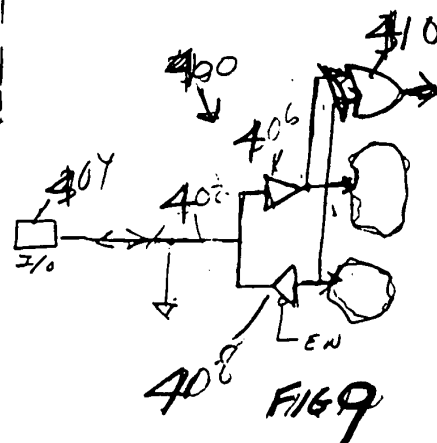


FIG 9